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Simulation of GaN HEMT with Wide-Linear-Range Transconductance

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Abstract—This paper presents a simulation study to achieve wide-linear-range transconductance of T-gate GaN HEMTs by introducing a δ -doped layer and a p-GaN back barrier. With optimized δ -doping density and location, the transconductance (g_m) and current gain cutoff frequencies (f_T) are ultra-flat and remain close to their peak values over a wide range of gate-source voltages (V_{gs}). In addition, a smaller absolute g_{m3} (third-order derivative of the I_{ds} - V_{gs} curve) over a wide range of V_{gs} is obtained in proposed HEMTs. These features are valuable in designing highly linear RF AlGaIn/GaN HEMTs.

Keywords—GaN HEMTs, Delta-doping, Back-barrier, Linear transconductance

I. INTRODUCTION

With excellent performance in high-power, high-voltage operations at microwave frequencies, GaN-based high electron mobility transistors (HEMTs) have been regarded as one of the most promising candidates for next-generation RF/microwave power amplifiers. Linearity is an important factor for RF power amplifiers due to the large dynamic range in the high variable envelop of the modulation signals. An ideally linear FET device would possess constant drain transconductance (g_m) over a wide range of input gate-source voltages [1]. The problems in achieving flat transconductance include (1) small absolute distance between 2DEG channel and gate [2], (2) increase of the access resistance at high drain current levels caused by quasi-saturation of electron velocity [3], and (3) large transverse electric-field (E-field) perpendicular to the channel and at high current levels [4]. It is reported that linearity can be enhanced by enlarging the absolute distance between 2DEG channel and gate through controlling the barrier thickness of GaN HEMT or implementing n-doped GaN cap above AlGaIn barrier or a combination [2]. Other methods of improving the linearity are reported by using field plate [5] or modifying the access resistance [3]. Moreover, a composite-channel design by inserting an AlGaIn layer with low-Al composition in the channel region was shown to exhibit higher linearity [4], due to the formation of a secondary channel that reduces transverse electric field in the 2DEG channel.

Another possible way to create a secondary channel is by introducing heavy n-type doping in the GaN layer close to 2DEG channel. Linearity improvement has been reported in AlGaIn/GaN HEMT with buffer structure of i-GaN/n-GaN/i-GaN layers (i-n-i buffer) [6], where the thin n-GaN in the

buffer modulates the band diagram to achieve a wider region of g_m profile. However, due to low uniform doping density of the n-GaN in the buffer, a secondary channel is not formed and the improvement of device linearity is limited. Delta doping (δ -doping) is an attractive technique to significantly increase doping density [7]. Si δ -doping in GaN layer can reach a sheet concentration as high as 1×10^{12} cm⁻² to 2×10^{13} cm⁻² [8, 9], while the Si uniform-doping in GaN can only achieve concentration up to a few 10^{19} cm⁻³ (sheet concentration of a few 10^{12} cm⁻²) [10, 11]. In addition, high density uniform doping in GaN film would introduce significant amount of strain which can cause cracking of layers [10], while Si δ -doping is a method to reduce tensile stress while obtaining high doping density [12].

In this work, we propose a T-gate AlGaIn/GaN HEMT with n-type δ -doped GaN buffer layer and p-GaN back barrier. The n-type δ -doping creates a secondary channel close to the 2DEG channel while the conduction band deep into the buffer is lifted up by the p-GaN back barrier to suppress buffer leakage and short channel effect. The most important feature for the proposed HEMT is that the transconductance (g_m) and cutoff frequencies (f_T) are ultra-flat and remain close to their peak values over a wide range of gate-source voltages (V_{gs}). In addition, a smaller magnitude of g_{m3} (third-order derivative of the I_{ds} - V_{gs} curve) over a wide range of V_{gs} has been obtained in the proposed HEMTs, which would be beneficial to achieve a higher third-order intercept point (IP3).

II. SIMULATION STRUCTURE

The schematic cross-section of the proposed T-gate AlGaIn/GaN HEMT is shown in Fig. 1. The sample includes a 1.8 μ m p-GaN back barrier with doping density of 1×10^{17} cm⁻³, followed by an unintentionally-doped (UID) GaN buffer and AlGaIn barrier layer. The barrier layer consists of a 3 nm UID Al_{0.3}Ga_{0.7}N spacer, a 20 nm Al_{0.3}Ga_{0.7}N n-doped region ($N_d = 2 \times 10^{18}$ cm⁻³), and a 2 nm UID Al_{0.3}Ga_{0.7}N cap layer. In this work, a heavily n-type δ -doped layer of 1 nm and doping densities of 6×10^{19} cm⁻³ (sheet doping density of 6×10^{12} cm⁻²) is introduced in GaN buffer with distance of 25 nm away from the AlGaIn/GaN interface. A conventional HEMT with 25 nm UID-Al_{0.3}Ga_{0.7}N barrier on 2 μ m UID-GaN buffer is also simulated for comparison. For all the simulated structures, the source/gate spacing (L_{gs}) is fixed at 1.0 μ m and gate/drain spacing (L_{gd}) is 2.0 μ m. The left/right part of T shape gate

L_g1/L_g3 are fixed at 0.05/0.1 μm , respectively, and center part of gate L_g2 varies from 0.1 to 0.3 μm . Moreover, the height of source/drain (H_2) is 0.2 μm , and the lower/upper height of gate (H_1/H_3) is 0.1/0.2 μm , respectively. The whole device is covered by 0.5 μm Si_3N_4 as surface passivation. The numerical simulations are carried out by SILVACO ATLAS. Shockley–Read–Hall, Fermi–Dirac statistics, Auger Recombination and self-heating models are enabled. The primary channel 2DEG density of the proposed HEMT is calculated by ATLAS to be $1.3 \times 10^{13} \text{ cm}^{-2}$, which agrees well with the sheet electron density reported from experiment [4].

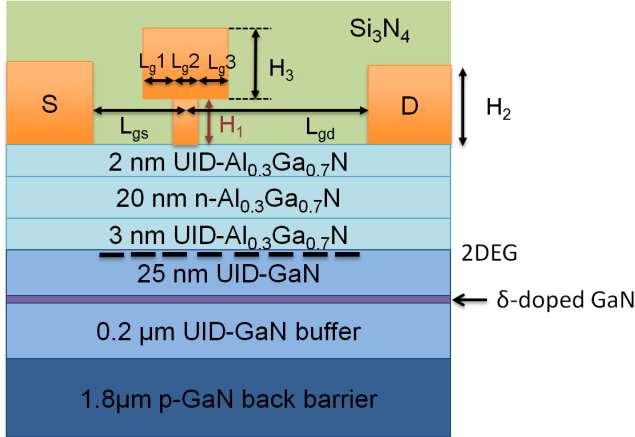


Figure 1 Schematic cross section of the proposed T-gate AlGaIn/GaN HEMT.

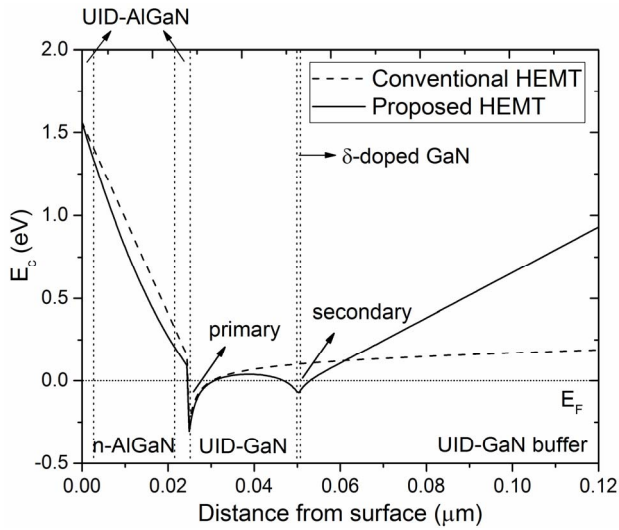


Figure 2 Conduction band diagrams of the proposed structure and conventional structure.

III. CHARACTERISTICS AND DISCUSSION

Fig. 2 shows the simulated conduction band diagram under the gate of the proposed HEMT and conventional HEMT. In the conventional HEMT, carriers are only located at the primary channel at AlGaIn/GaN interface. By inserting an n-type δ -doped GaN layer in the buffer, a secondary channel is created in the proposed HEMT. Under on-state condition, carriers can transit smoothly between the primary and secondary channels due to very low barrier height between them. On the other hand, the conduction band of conventional

HEMT is relatively flat in GaN buffer. Therefore, the electrons in the 2DEG channel would easily spill over to the buffer region under bias condition, leading to current flowing within the GaN buffer. The situation would be more serious when the gate length is small (under 0.2 μm), in which short channel effect would dominate. By introducing a p-GaN back barrier [13], the conduction band of proposed HEMT is pulled up, resulting in much better electron confinement in both primary and secondary channels. The carrier spill-over and short channel effect can be suppressed to some extent.

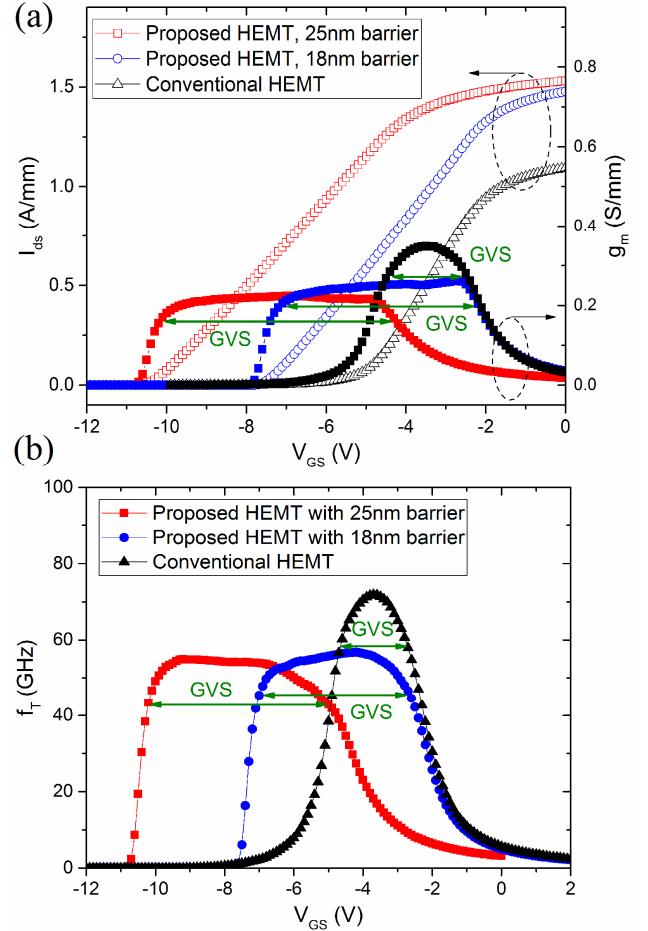


Figure 3 (a) transfer characteristics, (b) current gain cutoff frequencies of conventional structure and proposed structure with 18 or 25 nm barrier, $L_g2=0.2 \mu\text{m}$. The ac simulation was conducted at $f=1 \text{ MHz}$.

The transfer characteristics of the proposed HEMTs and conventional HEMT at $V_{ds}=3 \text{ V}$ are plotted in Fig. 3 (a). The thickness of AlGaIn barrier layer in conventional HEMT is 25 nm, while two different barrier thicknesses of 18 nm and 25 nm are introduced in the proposed HEMTs (13 nm and 20 nm n-AlGaIn n-doped layers) for comparison. The threshold voltages (V_{th}) of conventional HEMT and proposed HEMT with 18 nm and 25 nm barrier are -5.8 V, -7.9 V and -10.8 V, respectively. The negative shift of threshold voltages in proposed HEMTs is due to the formation of the secondary channel and therefore, more electrons under the gate electrode. It can be seen that nearly flat g_m are achieved over a wide gate bias (V_{gs}) range in the two proposed HEMTs, which is a desirable feature for

linear operation. It has been demonstrated that the increase in the access resistance as drain current increasing plays an important role in the decrease of g_m in AlGaIn/GaN HEMTs [14]. The flatter g_m in proposed HEMT can be ascribed to tailor of access resistance by forming the secondary channel, make it more independent of the drain current [3]. To quantitatively compare the linearity of transconductance, we adopt figure of merit of gate-voltage swing (GVS), which is defined as the range of the gate voltage where the device parameter drops to 80% of its peak value [15]. The corresponding GVS for g_m of the proposed HEMTs with 25 nm and 18 nm barrier are 5.6 V (from -10 to -4.4 V) and 4.8 V (from -7 to -2.2 V), which are ~ 2 and ~ 1.5 times higher than the 1.9 V (from -4.4 to -2.5 V) observed in the conventional HEMT, respectively.

The intrinsic cutoff frequency f_T is defined as the frequency when the current gain is unity and it is an important measurement for RF applications. Device transfer characteristics are simulated in ac mode, and g_m and gate capacitances C_{gd} and C_{gs} were extracted as a function of V_{gs} . The intrinsic cutoff frequency f_T was calculated by [13]

$$f_T = \frac{g_m}{2\pi C_{gs} \sqrt{1 + 2(C_{gd}/C_{gs})}} \quad (1)$$

Fig. 3 (b) shows f_T as a function of V_{gs} for conventional HEMT and proposed HEMTs. Relatively flat f_T is obtained for the two proposed HEMTs from low to high current levels, while the conventional HEMT shows the peak at $V_{gs} = -3.7$ V and drops significantly at high current levels. The corresponding GVS for f_T in the conventional HEMT is 2.1 V (-4.7 to -2.6 V). And GVS in the proposed HEMTs with 18 nm and 25 nm barrier are 4.4 V (from -7 to -2.6 V) and 5.1 V (from -10.2 to -5.1 V), respectively. The highly flat f_T of proposed HEMTs is associated with higher linearity for both g_m and gate-source capacitance.

The δ -doping density and location to the primary channel has to be carefully optimized to achieve flat g_m over wide range of V_{gs} . Fig. 4 (a) and Fig. 4 (b) show simulated g_m as a function of δ -doping density and delta doping location in proposed HEMT with 25 nm barrier. The g_m of conventional HEMT is also added for comparison. Firstly, the delta doping location is fixed at 25 nm away from AlGaIn/GaN interface (primary channel). As the sheet density of delta doping increases from $4 \times 10^{12} \text{ cm}^{-2}$ to $6 \times 10^{12} \text{ cm}^{-2}$, it can be seen in Fig. 4 (a) that the original g_m peak around $V_{gs} = -5$ V reduces and another g_m peak at negative side of V_{gs} increases gradually, which is due to the modulation of access resistance by conduction in secondary channel. However, as delta doping density further increases to $8 \times 10^{12} \text{ cm}^{-2}$, g_m shows a two-peak behavior rather than a flat region. This double-hump behavior in the g_m versus gate bias curve is commonly observed in double channel GaN HEMTs [16], which corresponds to the effective gate modulation of the primary and secondary channel respectively. The two-peak behavior would cause large on-resistance and kinks in the linear region of the I_{ds} - V_{ds} output curves [17], which is harmful for device linearity. On the other hand, the two-peak behavior

is also observed when δ -doping density is fixed at $6 \times 10^{12} \text{ cm}^{-2}$ while δ -doping location to primary channel is beyond 30 nm (shown in Fig. 4 (b)). To achieve linear transconductance, the optimized δ -doping sheet density and location to AlGaIn/GaN interface are determined to be $6 \times 10^{12} \text{ cm}^{-2}$ and 25 nm, respectively.

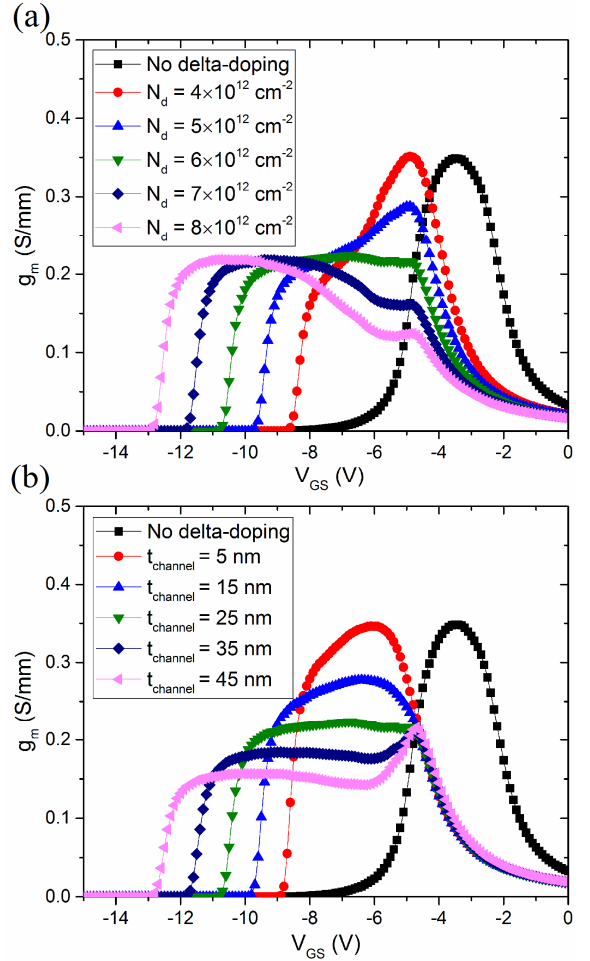


Figure 4 Simulated transconductance (g_m) as a function of (a) delta doping density in buffer and (b) delta doping location to AlGaIn/GaN interface in proposed HEMT with 25 nm barrier.

In order to achieve a high third-order intercept point (IP3) for linear power amplifier application, the magnitude of third derivative of the I_{ds} - V_{gs} curve (g_{m3}) should be minimized [18]. Fig. 5 (a) plots the g_{m3} as a function of V_{gs} in conventional HEMT and proposed HEMT without p-GaN back barrier. It can be seen that the proposed devices without back barrier has an overall lower magnitude g_{m3} curve over a wider range of V_{gs} than it in conventional device. The g_{m3} peak reductions are $\sim 73\%$ and $\sim 65\%$ in devices with 25 nm and 18 nm barrier, respectively. For the proposed HEMTs with back barrier, the carrier confinement is enhanced thus subthreshold slope of transfer curve becomes sharper. This results in an increase of g_{m3} peak near the threshold voltage (shown in Fig. 5 (b)), however, the g_{m3} peak value is still at least $\sim 28\%$ and $\sim 36\%$ lower in proposed HEMT with 18 nm and 25 nm than in conventional HEMT, respectively. It should be noted that more

detailed two-tone intermodulation characteristics has to be carried out to further confirm linearity improvement for power amplifier application.

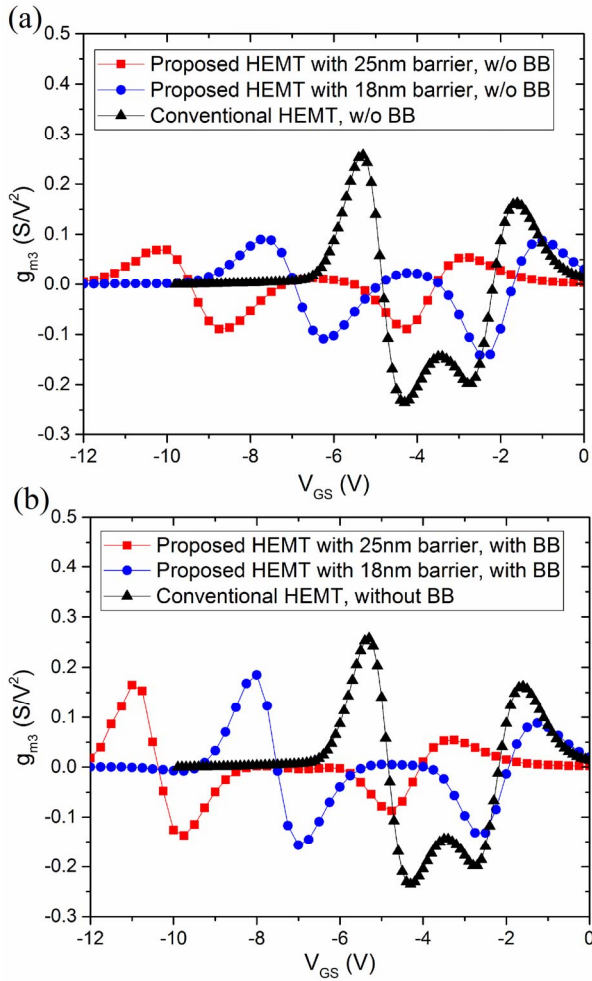


Figure 5 Simulated g_{m3} as a function of gate-source voltage in conventional HEMT and proposed HEMT (a) without back barrier and (b) with back barrier

IV. CONCLUSION

In conclusion, we proposed an AlGaIn/GaN HEMT with an n-type delta-doped GaN buffer layer and a p-GaN back barrier. DC and AC simulations show that transconductance (g_m) and cutoff frequencies (f_T) can be maintained near their peak values with wide range of V_{GS} (from low to high current levels). And the flat region of g_m can be modified by delta-doping location and density. A lower magnitude g_{m3} over a wide range of V_{GS} has been obtained in proposed HEMTs than conventional HEMT. These results are especially valuable for designing highly linear RF AlGaIn/GaN HEMTs.

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